

FIG.2

(b)	260,	- 79,	Ο,	-8,	- 2,	- 3,	-4,	1,	
2× (b)	520,	158,	Ο,	16,	4,	6,	8,	2,	•••
(d)	16,	12,	14,	14,	18,	24,	49,	72,	•••
DECIDED RESULT OF COMPARISON	1,	1,	0,	1,	0,	Ο,	Ο,	0,	•••

FIG.3

(e) PREDICTING REGISTER

_	-							_
	1	1	1	0	0	0	0	0
ı	1	1	0	0	0	0	0	0
ı	0	1	0	0	0	0	0	0
ı	1	0	0	0	0	0	0	0
I	0	0	0	0	0	0	0	0
ı	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0
I	0	0	0	0	0	0	0	0

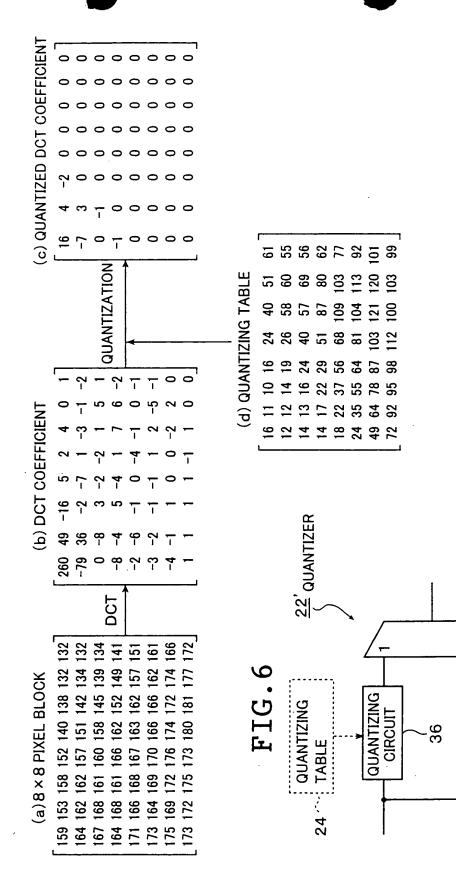
FIG.4

	O -RUN LENGTH	(b)	(c)
1	0	260	16
2	0	49	4
3	0	- 79	- 7
4	1	36	. 3
5	0	- 16	- 2
6	2	- 8	- 1
7	0	- 8	- 1
8	EOB		

→ QUANTIZER QUANTIZING 22 24 QUANTIZING QUANTIZER <u></u> DATA QUANTIZING DEVICE 42 RAM 20 **→**COMPARATOR RAM <u>9</u> 20 28, **PRIOR ART** 1-D DCT FIG. 7 <u>∞</u> FIG.5 SHIFT CIRCUIT 26 RAM 16 1-D DCT 8 14 1-D DCT RAM 16 (a) 1-D DCT

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FIG.8



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